I MADE A CELL PHONE!

(Dont tell the FCC kthx)

Kevin Lynagh !! con 2015



Smartphones





Solution: Build my own awesome phone

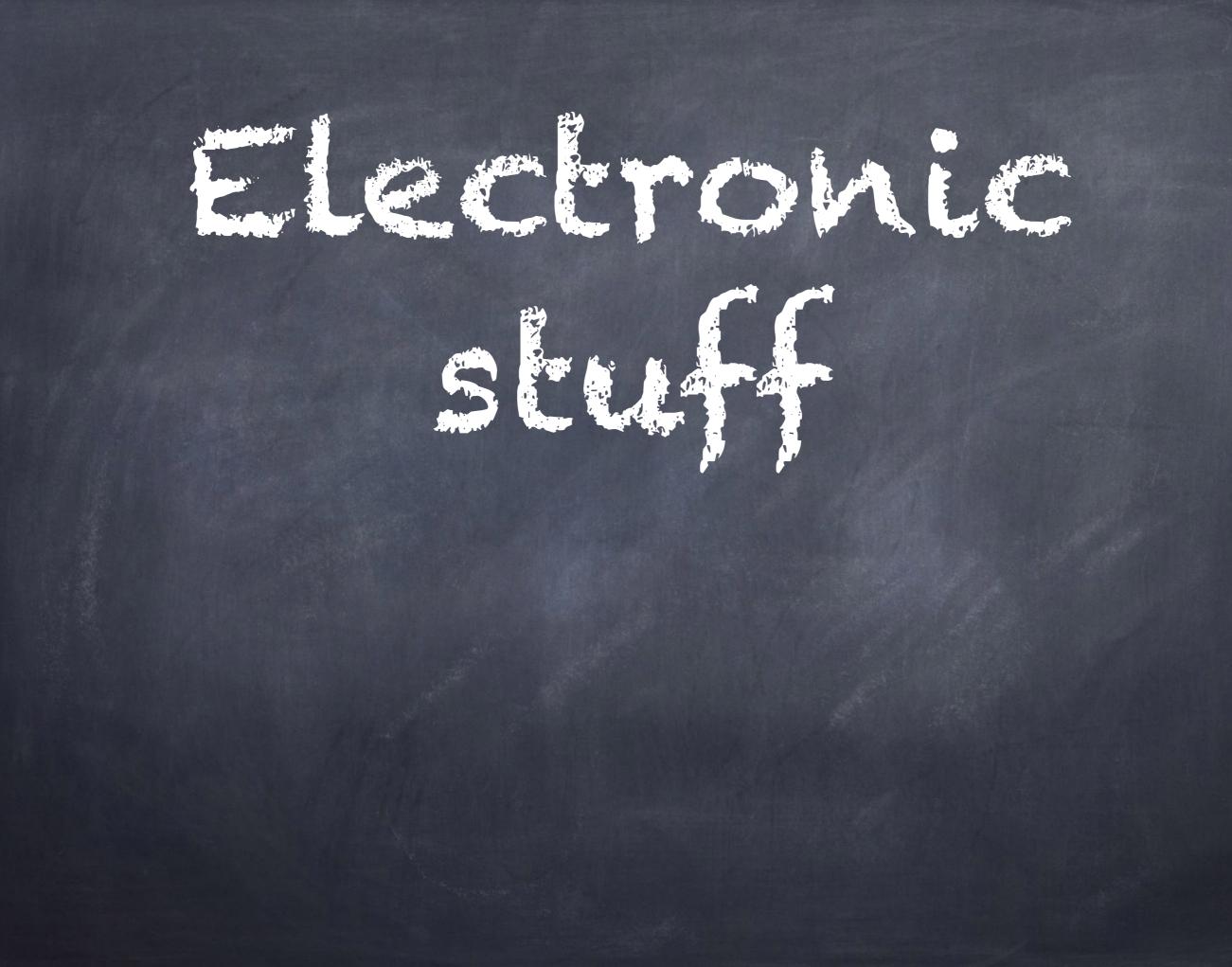






Building a phone:

Electronic stuff
 Software stuff
 Ind. Design stuff



J-Lab Fall 2007 Lab Ticket 3

Name: Kevin Lynege

11:10 - 11:20. You have 20 minutes to complete this closed-book lab-ticket.

1. Determine an expression for the gain $G = \frac{V_{out}}{V_{in}}$ of the non-inverting amplifier shown in Fig. 1. The triangles indicate the ground (0 V) level, relative to which V_{in} and V_{out} are Assume V = V = V = Vin measured.

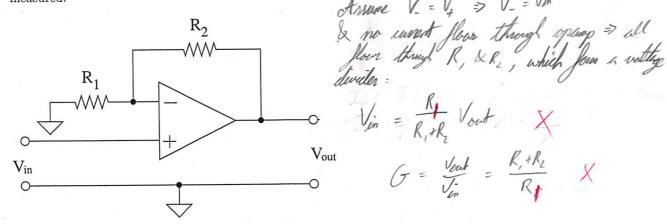


Figure 1: Non-inverting amplifier

Derive an expression for the input impedance, $Z_{in} = V_{in}/I_{in}$, for the circuit shown in 2. Fig. 2. (Hint: Use the Golden Rules for operational amplifiers and Kirchoff's laws.)

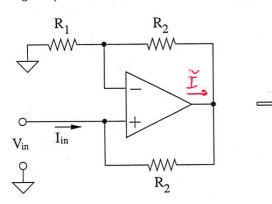


Figure 2: What is the input impedance?

Op Amps source aurent !!

0

Since no unent though the of any, it all must flow through R. before reaching ground V=Vin that flows through gwen $\Delta V = (R_1 + R_2) I$ =0 which gives an input impedance Z= T = 00 (AKA very high, which (In talk) is why op anyon we see weful)

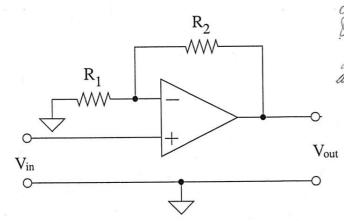
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Ansume $V_{-} = V_{+} \Rightarrow V_{-} = V_{in}$ & no current floor through optimp \Rightarrow all floor through $R, \& R_{2}, which flow a with$ dividen : $V_{in} = \frac{R_{+}}{R_{+}+R_{2}} V_{out} = \frac{K_{+}+R_{2}}{R_{+}} \times \frac{K_{+}}{R_{+}}$

Figure 1: Non-inverting amplifier

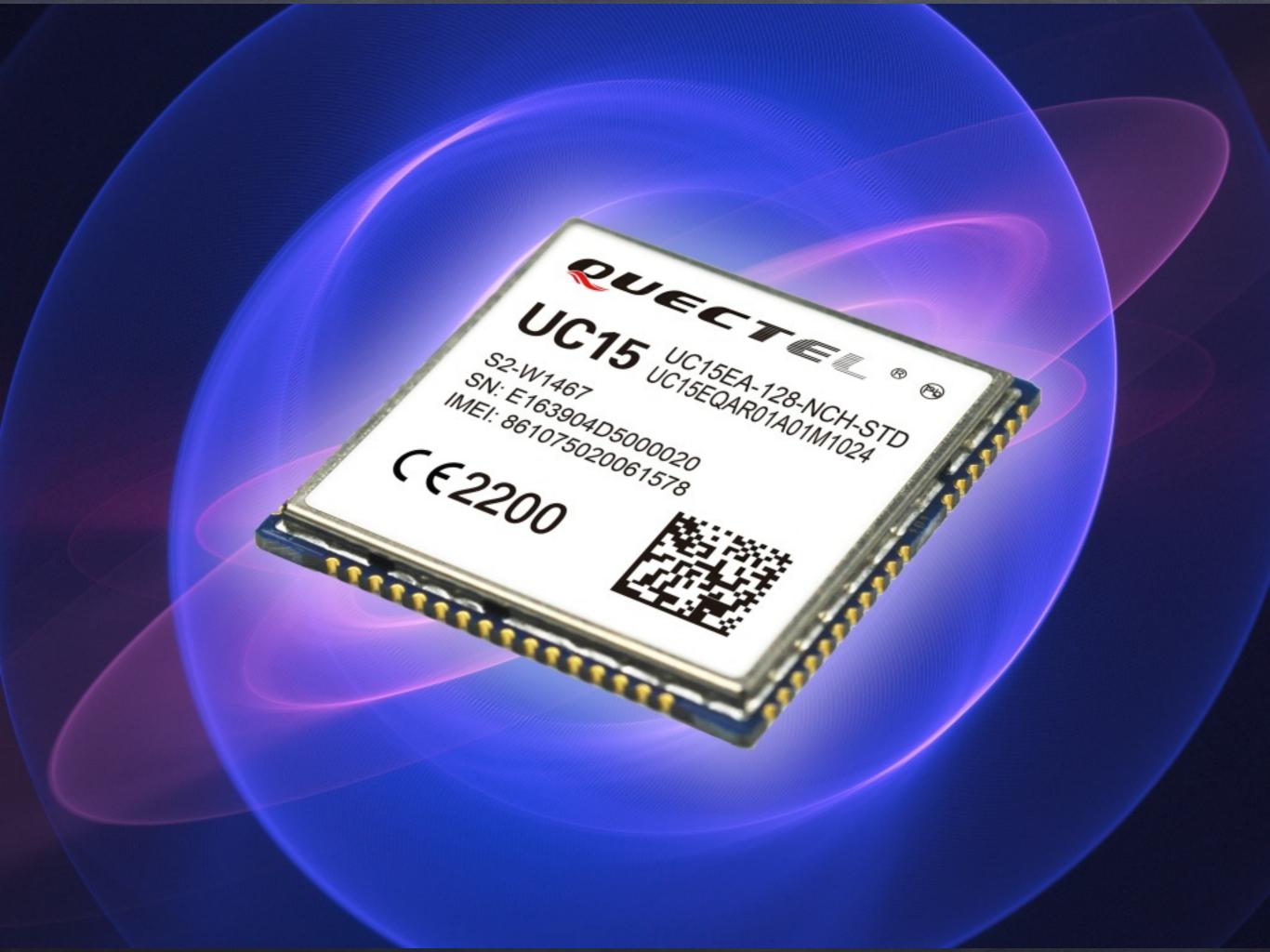
2. Derive an expression for the input impedance, $Z_{in} = V_{in}/I_{in}$ for the circuit shown in Fig. 2. (Hint: Use the Golden Rules for operational amplifiers and K schoff's laws.)

Since no ment R_2 R₁ though the of way, must flow through R. +R. +R. ř reaching spround I_{in} Vin V=Vin Ŷ ww R_2 Through Figure 2: What is the input impedance? mps sou de $\Delta V = \left(R_{1} + R_{1} \right) I$ impedance (AKA very high, which (In tall) is why op anyon we see weful) 00

DHAVENO IDEA WHAT FM DOING

memegenerator.net

Needed: Cellular computer chip thingy





UC15 Hardware Design

UMTS/HSDPA Module Series

Rev. UC15_Hardware_Design_V1.3

Date: 2014-04-21



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UC15 Hardware Design

UMTS/HSDPA Module Series

Rev. UC15_Hardware_Design_V1.3

Date: 2014-04-21



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The following figure shows the reference design of the 8-pin USIM card.

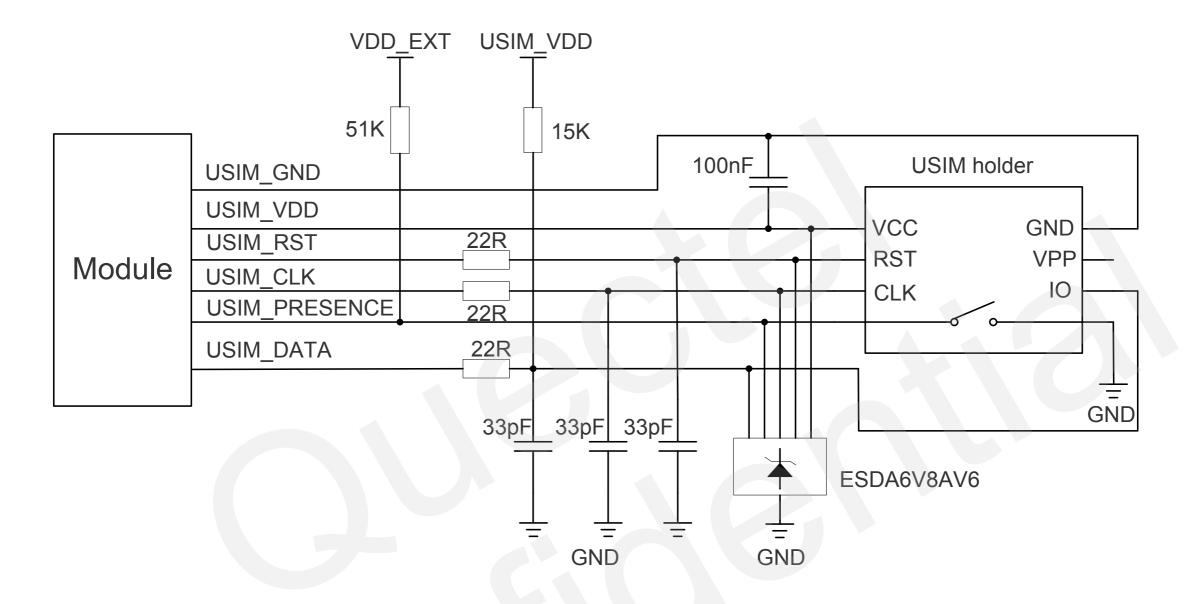


Figure 30: Reference Circuit of the 8-Pin USIM Card



Rubra Penta-band SMD Antenna Part No. A10393

gigaNOVA®

Product Specification

1 Features

- GSM/UMTS antenna supporting up to 5 frequency bands ٠
- Patented MDA antenna technology provides resistance to de-tuning
- High efficiency
- Easy to integrate
- Intended for SMD mounting
- Supplied in tape on reel

Rubra supports the following communication standards:

GSM/GPRS/EDGE	CDMA2000 1XRTT/EV-DO/EV-DV	UMTS WCDMA/HSPA	Other Standards
GSM850 (E)GSM900 GSM1800 (DCS) GSM1900 (PCS)	Band Classes: 1,2,3,4,6,8,9,12,14,15	Bands I – VI Bands VIII – X	Korean PCS DECT TD-SCDMA AWS

2 Description

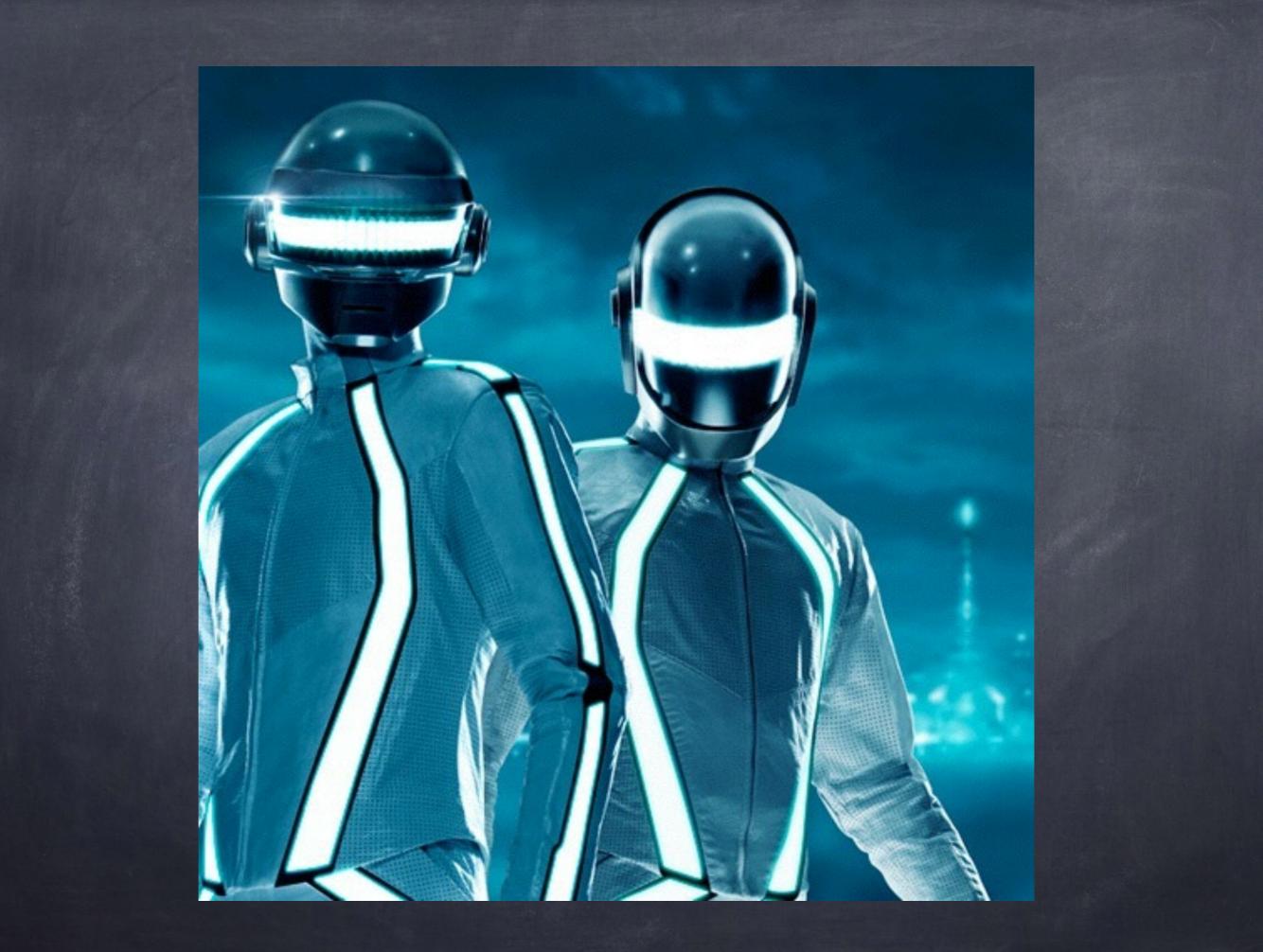
Rubra uses a ground plane in order to radiate efficiently, but this ground plane must not extend underneath the antenna itself.

The antenna uses a matching circuit to achieve optimized results for the specific frequency bands that are required. This product specification shows the performance of the antenna when optimized to cover a typical penta-band reception: GSM850/900/1800/1900 and WCDMA.

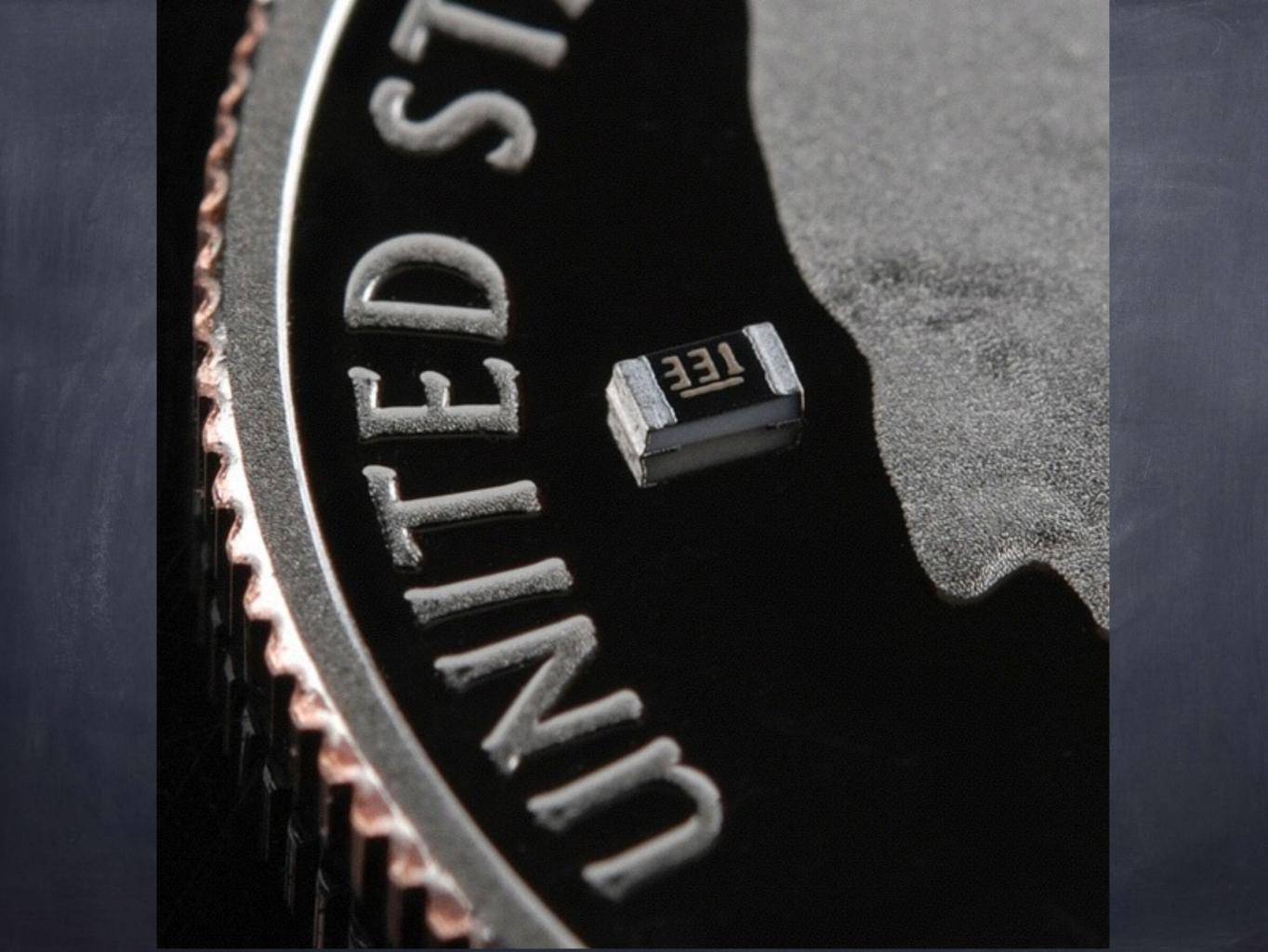
3 Applications

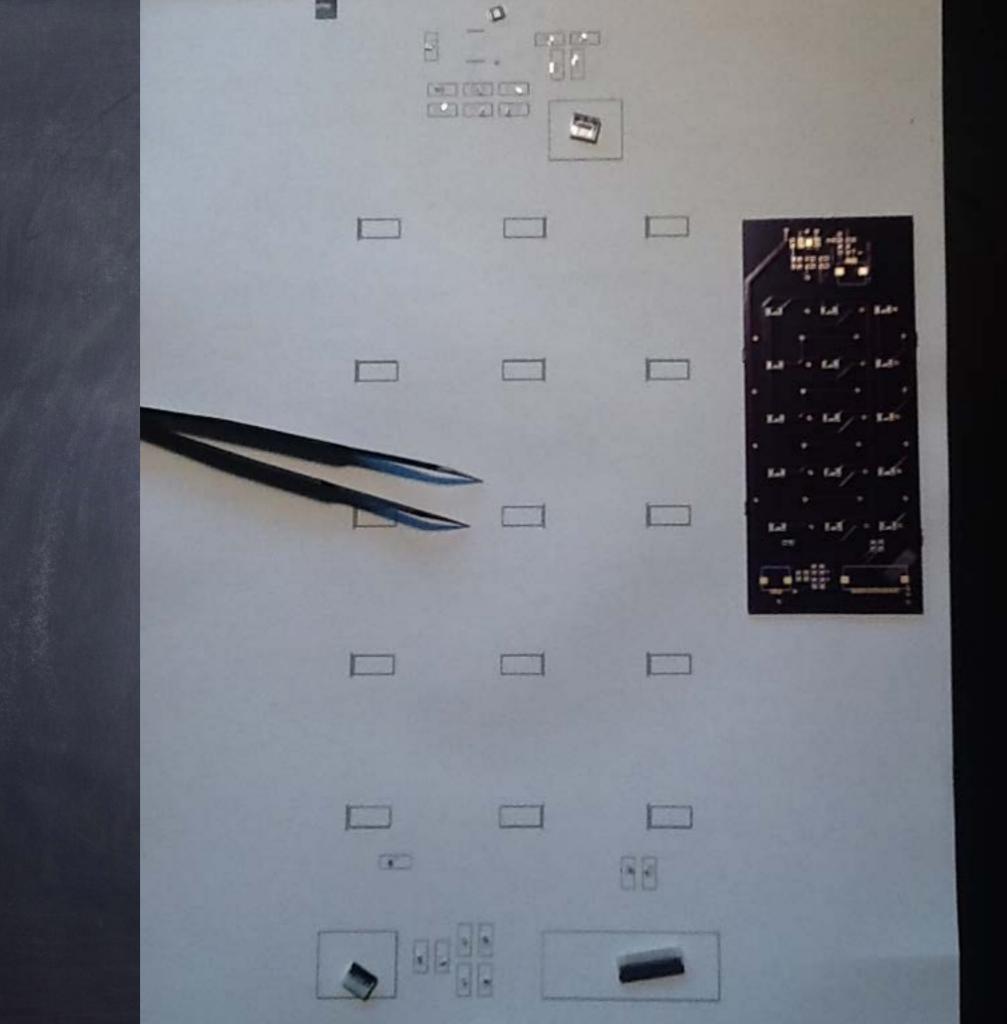
- Mobile handsets
- Femto / Pico base stations
- Tracker devices
- Machine to machine communication
- Remote monitoring

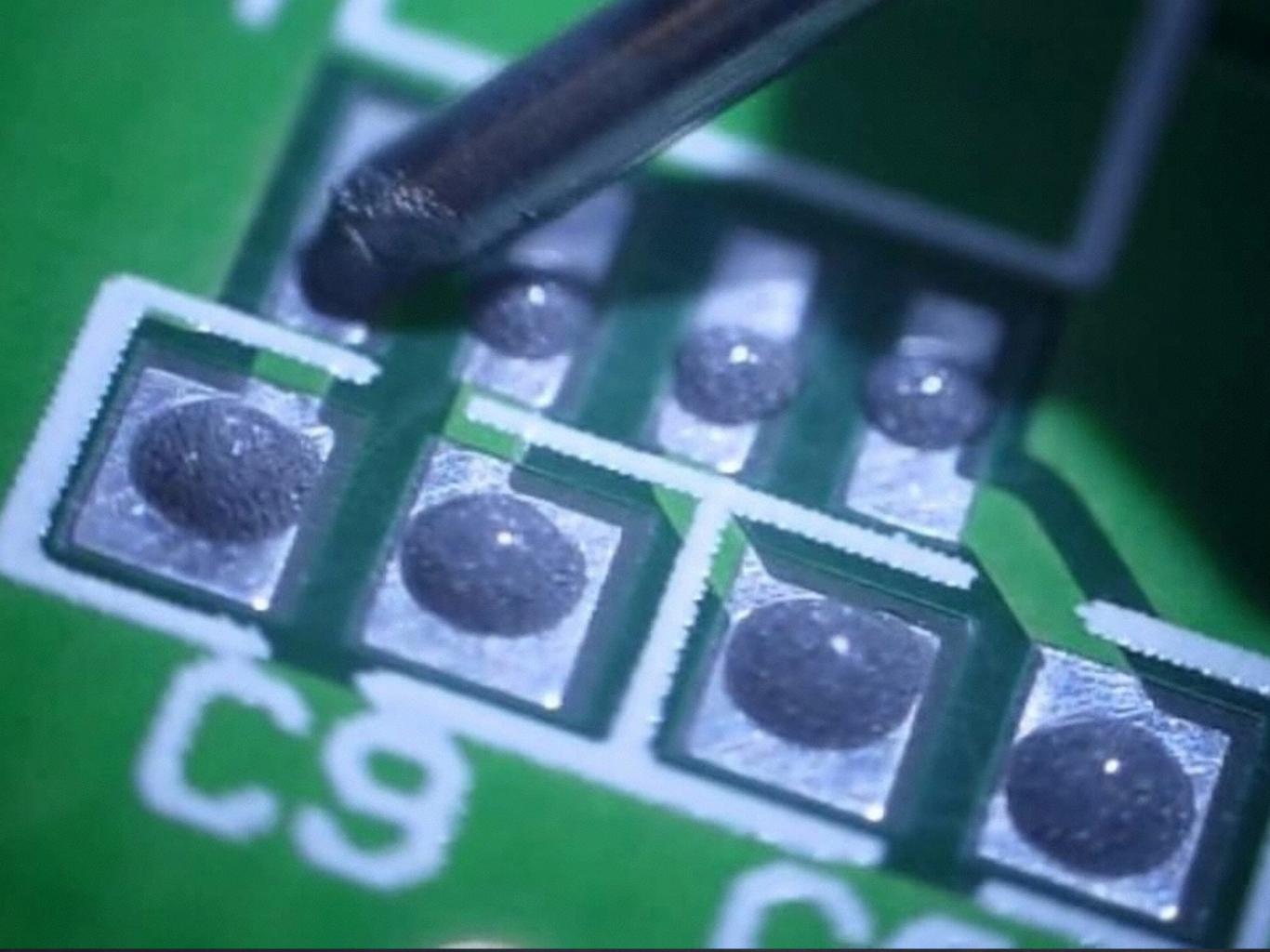






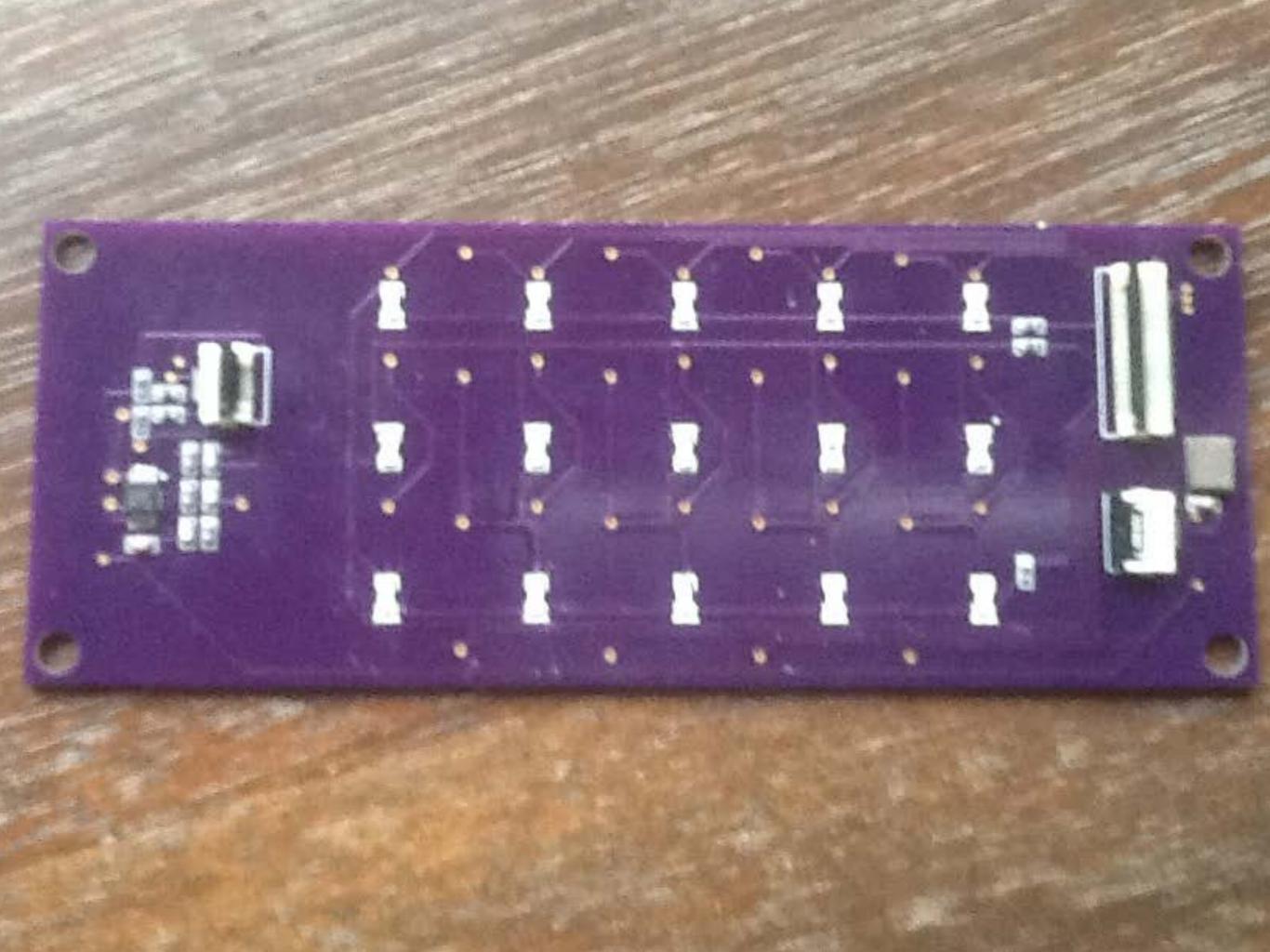














Tiny compuler, big responsibilities

+ delect buttons presses + blink the lights + ask the cellular chip 4 calls + delect low balleries, cellular connectivity, &c.

All prices are in US dollars				
Digi-Key Part Number	ATMEGA1284-AURCT-ND	Price	Unit	Extended
	Digi-Key Stock: 2,042	Break	Price	Price
Quantity Available	Can ship immediately	Price Unit Break Price Break Price Break 1 Break 1 Break 1 1 8.03000 10 7.22400 1284-AUR 25 1284-AUR 100 1284-AUR 100 11284-AUR 100 1284-AUR 100 1128KB 250 128KB 250 128KB 250 100 5.94770 100 4.98240 / RoHS 1,000	8.03	
Manufacturer	Atmel	10	7.22400	72.24
Manufacturer Part		25	6.57040	164.26
Number	ATMEGA1284-AUR	ND Price Break 1 1 1 10 25 100 25 100 250 500 500	5.94770	594.77
Description	IC MCU 8BIT 128KB	250	5.44952	1,362.38
Description	Instrumentation Can ship immediately Infacturer Atmel Inter Part ATMEGA1284-AUR Inter Part ATMEGA1284-AUR Inter Part IC MCU 8BIT 128KB Ic MCU 8BIT 128KB FLASH 44TQFP Status / Lead free / RoHS	500	4.98240	2,491.20
Lead Free Status / RoHS Status	Lead free / RoHS Compliant	1,000	4.32846	4,328.46

This part can be programmed by Digi-Key; for details please contact our custom department at 1-800-344-4539 x5725 or custom.orders@digikey.com

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Product Training Modules	MCU Product Line Introduction megaAVR Introduction Software Framework (ASF)	ATMEGA1284-AU-ND	ATMEGA1284-AU	Atmel	Tray 🕜	

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	Digi-Key Stock: 2,042	Break	Price	Price	
Quantity Available	Can ship immediately	1	8.03000	8.03	
Manufacturer	Atmel	10	7.22400	72.24	
Manufacturer Part		25	6.57040	164.26	
Number	ATMEGA1284-AUR	100	5.94770	594.77	
Decorintion	IC MCU 8BIT 128KB	250	5.44952	1,362.38	
Description	FLASH 44TQFP	500	4.98240	2,491.20	
Lead Free Status / RoHS Status	Lead free / RoHS Compliant	1,000	4.32846	4,328.46	

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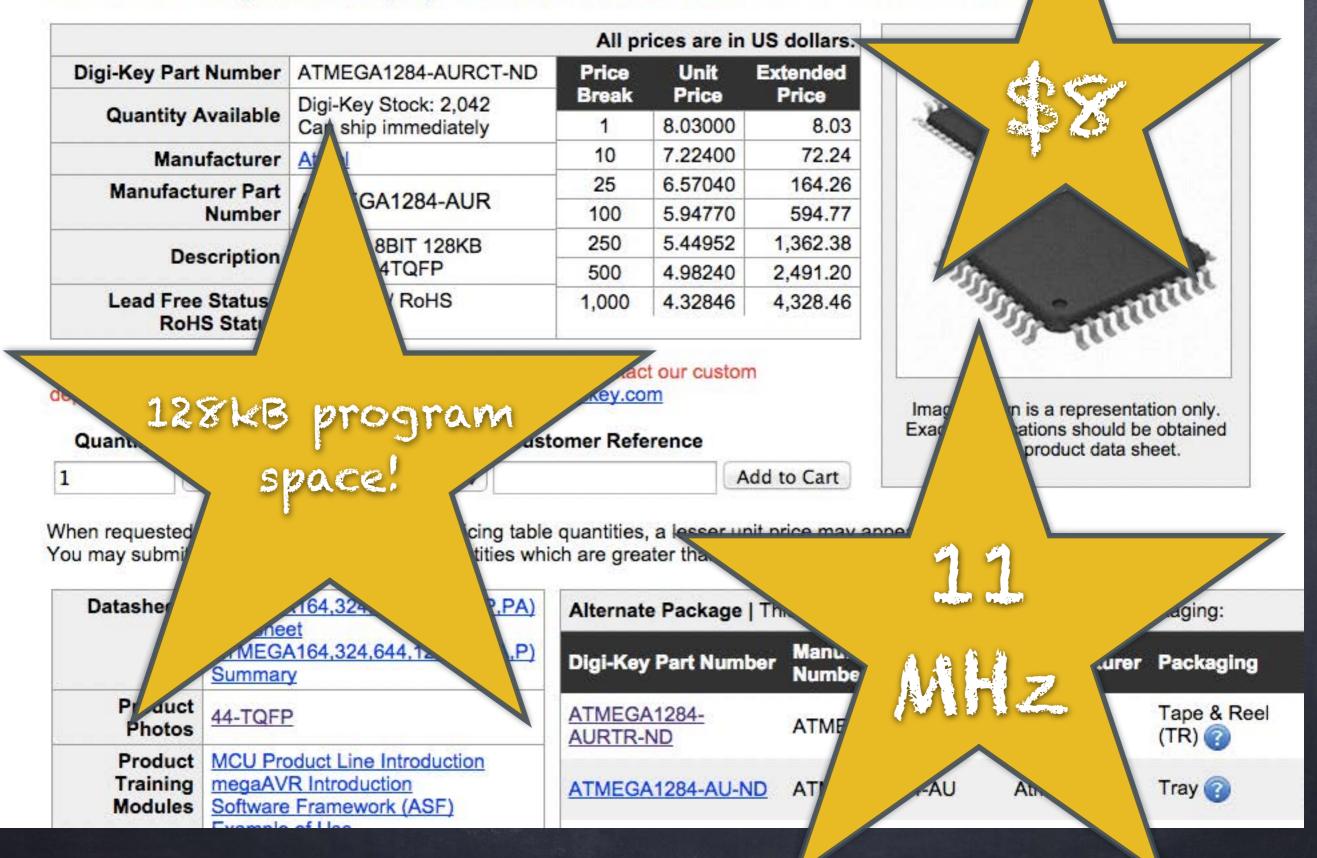
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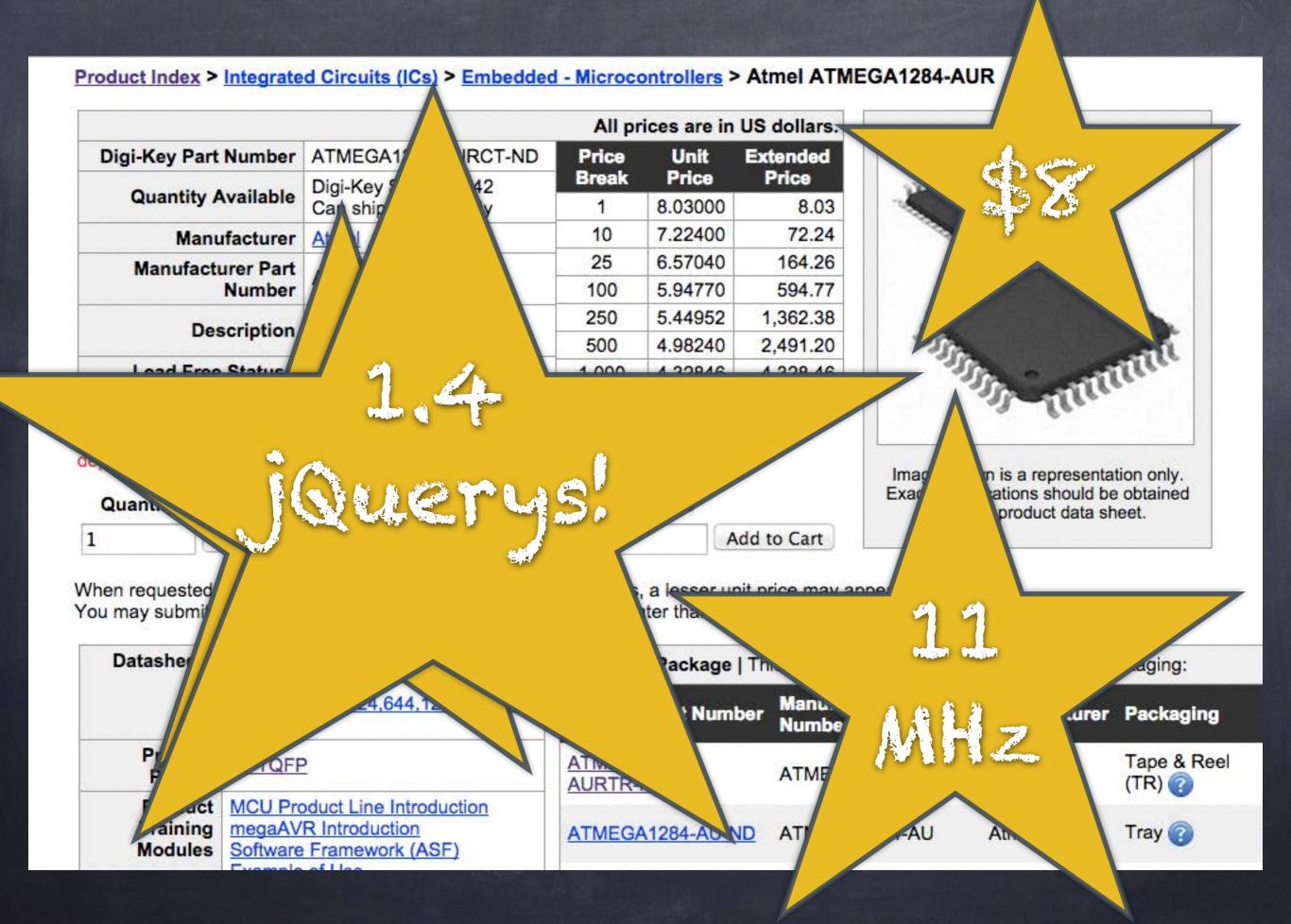
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	ATMEGA164,324,644,1284(A,PA,P) Summary	Digi-Key Part Number	Manu. Numbe		urer Packaging
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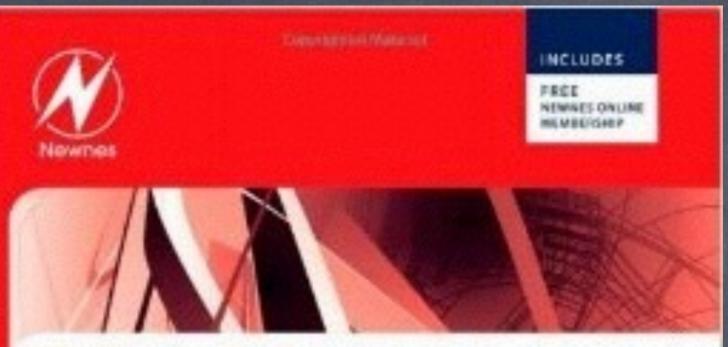




No Operating System

+ no Ehreads
+ no garbage collection
+ no sleep();
+ no callbacks

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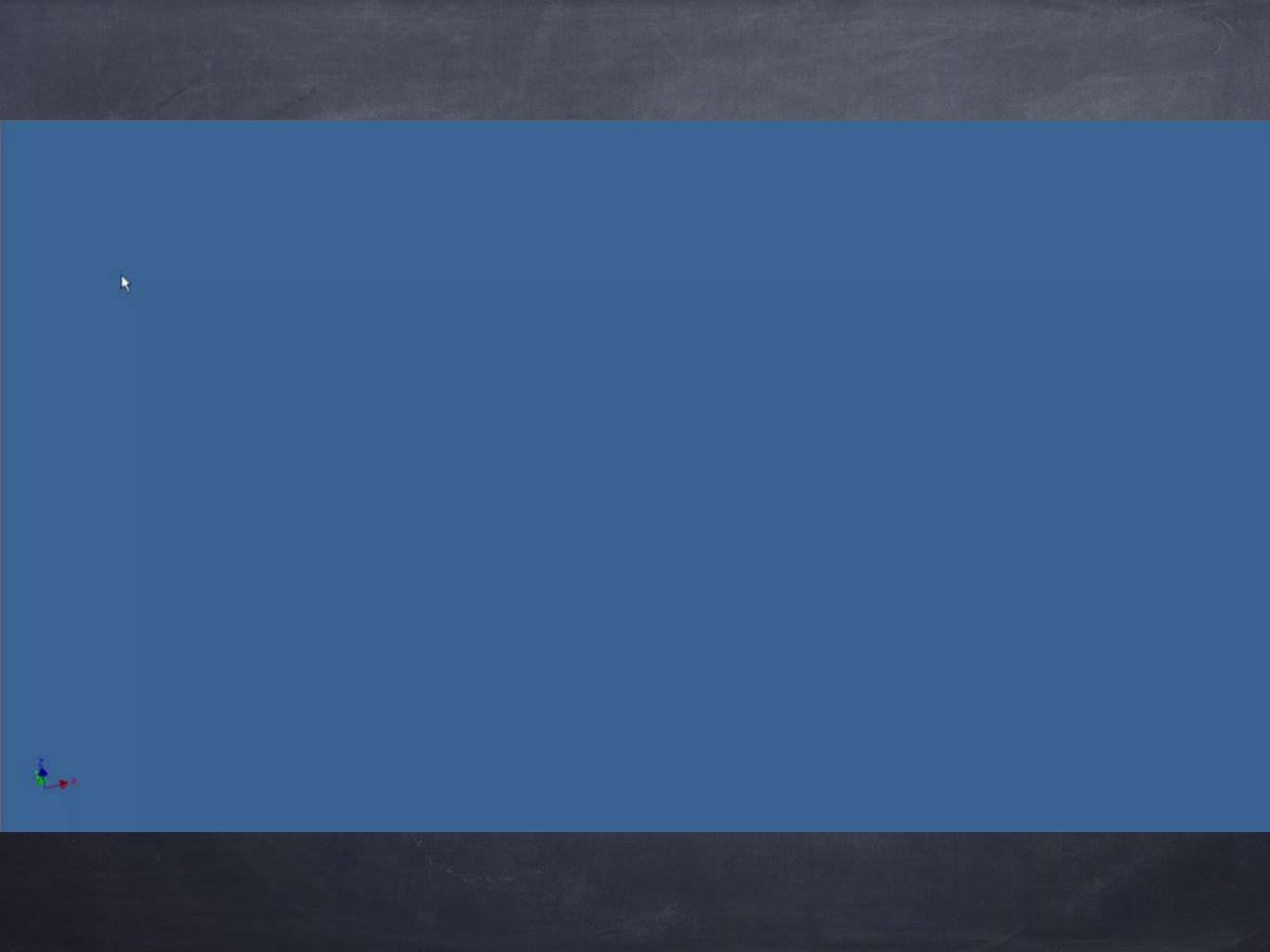
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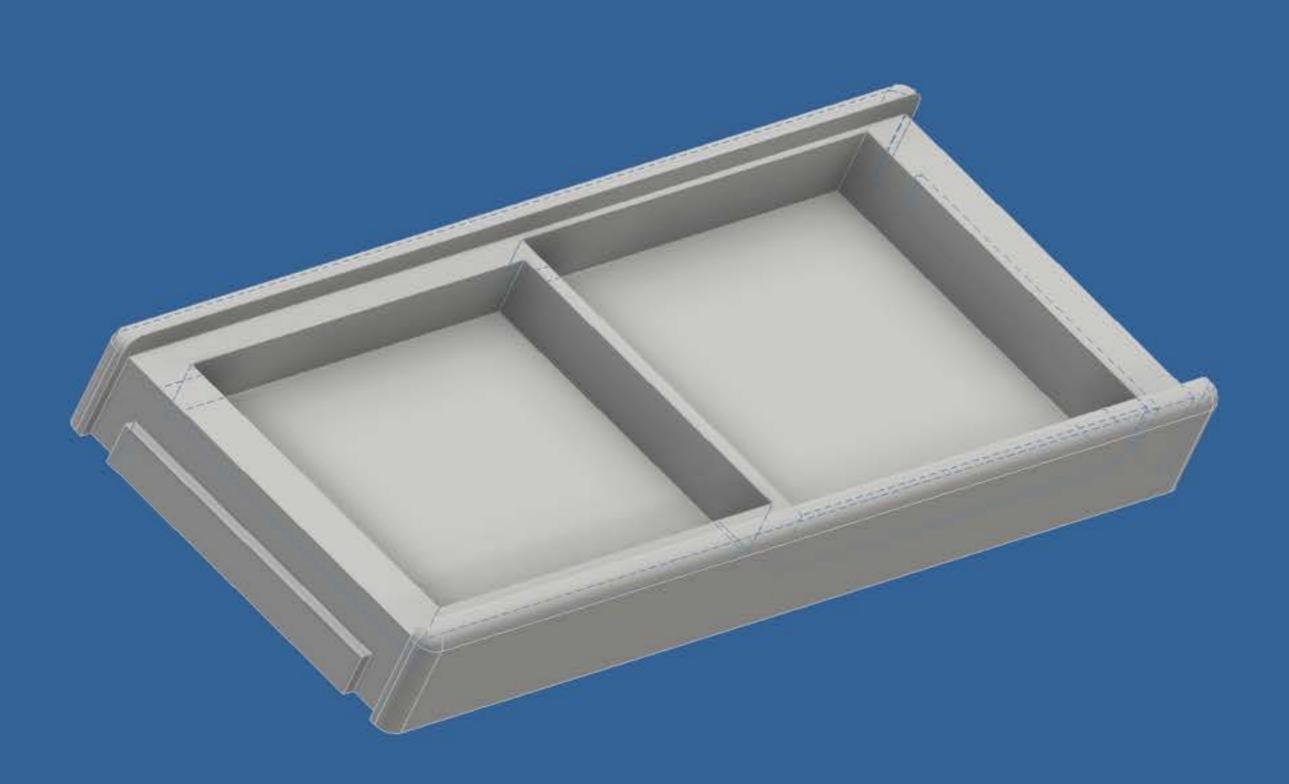
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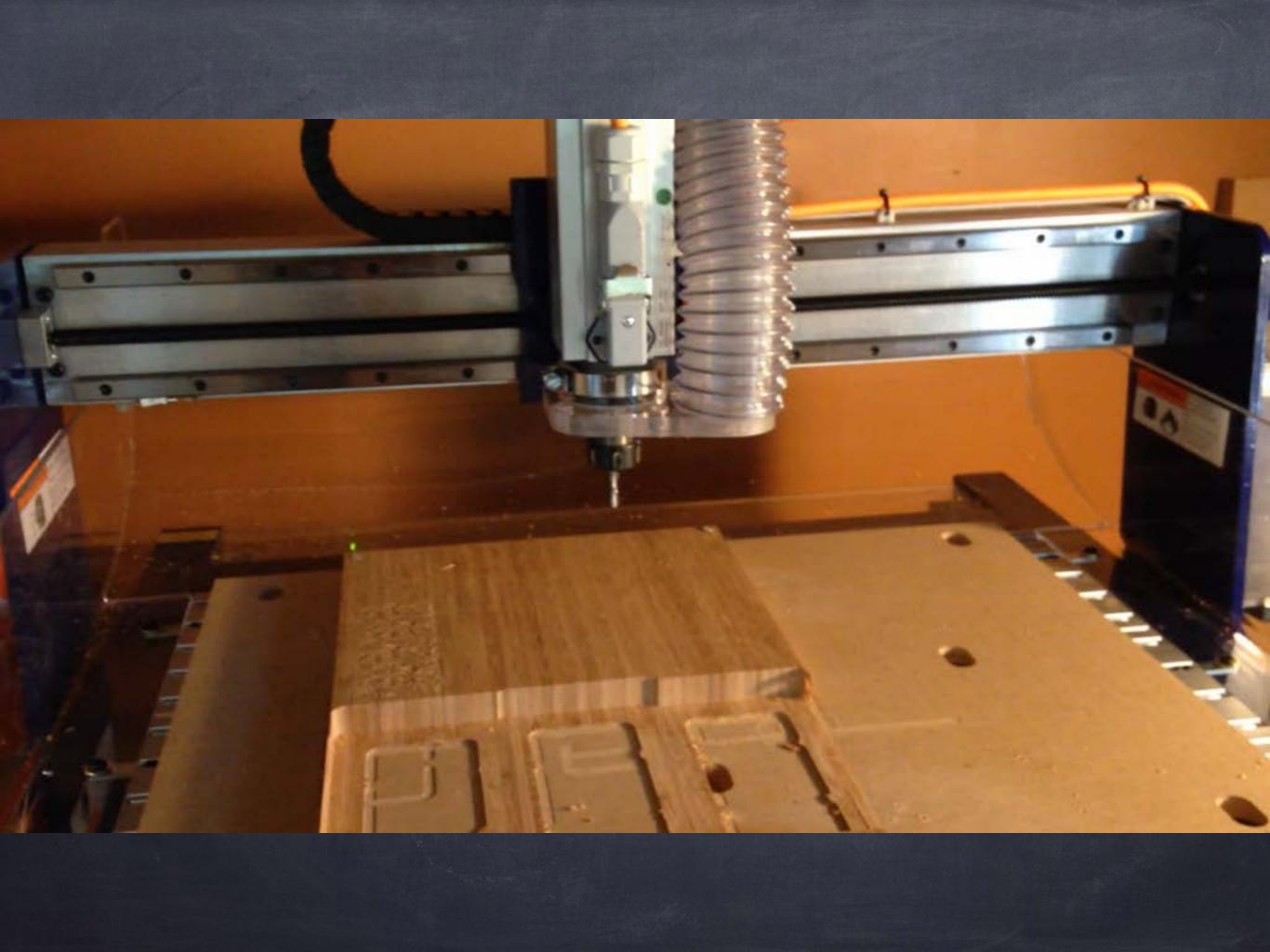




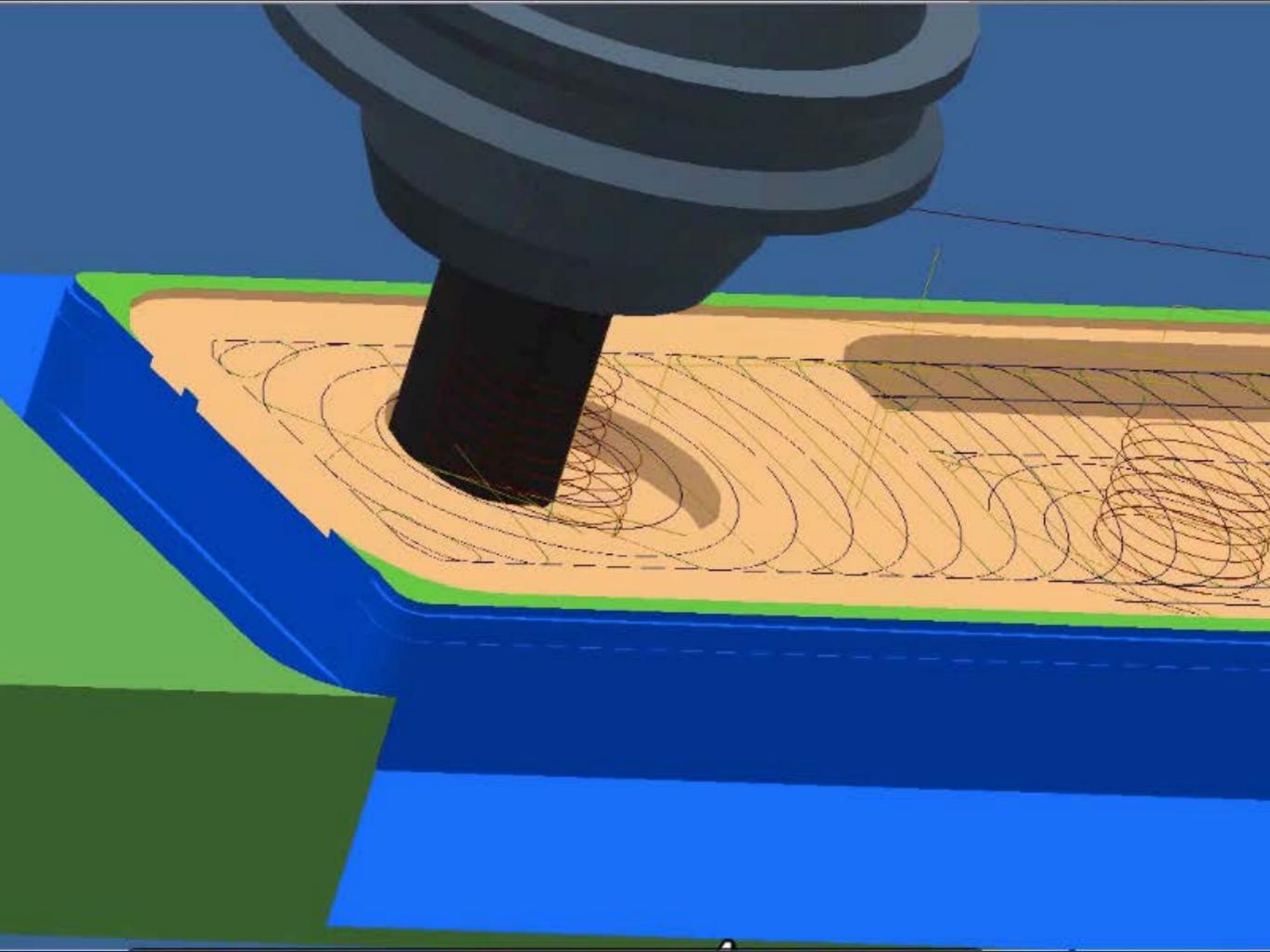
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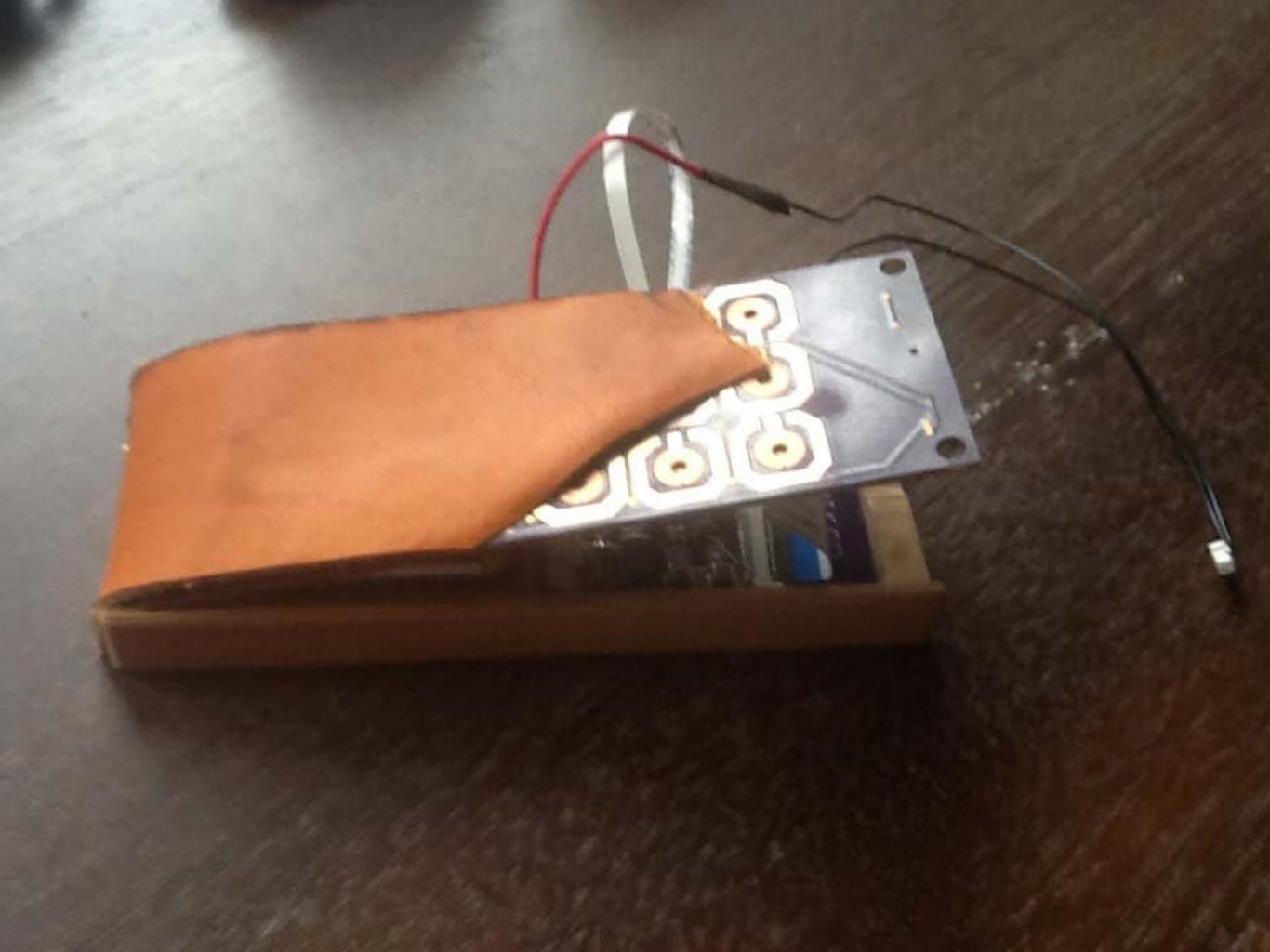












I'm 350 hours in.

+ 80h research / sketching / thinking + 70h software

+ 70h electrical engineering + 30h design / manufacturing + 30h workin' with my hands

Marence Sturf LS ACTION



Thanks.

elynaghk

(secret mailing list)